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O | PDocket No. A558MW (121116.00011)

APR 1 1 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Kenneth R. Philpot

Serial No.:

09/873,059

Filing Date:

06/02/2001

Title:

ENHANCED PERFORMANCE SURFACE MOUNT SEMICONDUCTOR

PACKAGE DEVICES AND METHODS

TRANSMITTAL

Commissioner for Patents Washington, D.C. 20231

EXPRESS MAIL NO. EL 915821130 US

DATE OF DEPOSIT: April 11, 2003

Dear Sir:

Enclosed please find the following documents for filing with the U.S. Patent and Trademark Office:

- 1. Amendment;
- 2. Petition for Extension of Time;
- 3. Check in the amount of \$930.00 to pay for the EOT; and
- 4. IDS with references and statement.

Respectfully submitted,

Michael G. Cameron

Reg. No. 50,298

Jackson Walker L.L.P. 2435 North Central Expressway, Suite 600

Richardson, Texas 75080 Phone: (972) 744-2934 Fax: (972) 744-2909 Docket No. A558MW (121116.00011)

PATENT

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Applicant:

Philpot, Kenneth R.

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ENHANCED PERFORMANCE SURFACE MOUNT

SEMICONDUCTOR PACKAGE DEVICES AND METHODS

Art Unit:

2827

Examiner:

Luan Thai

Commissioner for Patents

Box: Amendment

Washington, D.C. 20231

Dear Sir or Madam:

I hereby certify under 37 CFR 1.8 that this correspondence is being deposited with the United States Postal Service by Express Mail No. EL915821130US addressed to: Commissioner for Patents, Washington, D.C. 20231.

Printed Name:

Signature

AMENDMENT UNDER 37 C.F.R. 1.111

Election/Restriction

Applicant's election without traverse of group 1, claims 1-20 in Paper No. 7 filed 9/24/02 is acknowledged.

Responsive to the Office Action mailed October 22, 2002, please amend the above identified application as follows:

In the Claims:

Claim 1:

Please amend Claim 1 as follows:

(Once amended) 1. A surface-mount semiconductor device package comprising:

a substrate having at least one planar surface;

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